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PATENT APPLICATION  
No. 398,138  
SEP 17 2002  
TECHNOLOGY CENTER 2600

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of: Joel R. Goergen

Serial No.: 10/068,622

Examiner:

Not yet assigned

Filing Date: February 5, 2002

Group Art Unit: 2661

Title: PASSIVE TRANSMISSION LINE EQUALIZATION USING CIRCUIT BOARD THROUGH-HOLES

Date: July 31, 2002

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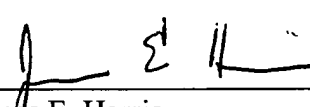
Assistant Commissioner for Patents  
Washington, D.C. 20231

REQUEST FOR NONPUBLICATION - RESCINDED

I hereby **rescind** the previous request that the above-identified application not be published under 35 USC §122(b).

Respectfully submitted,

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